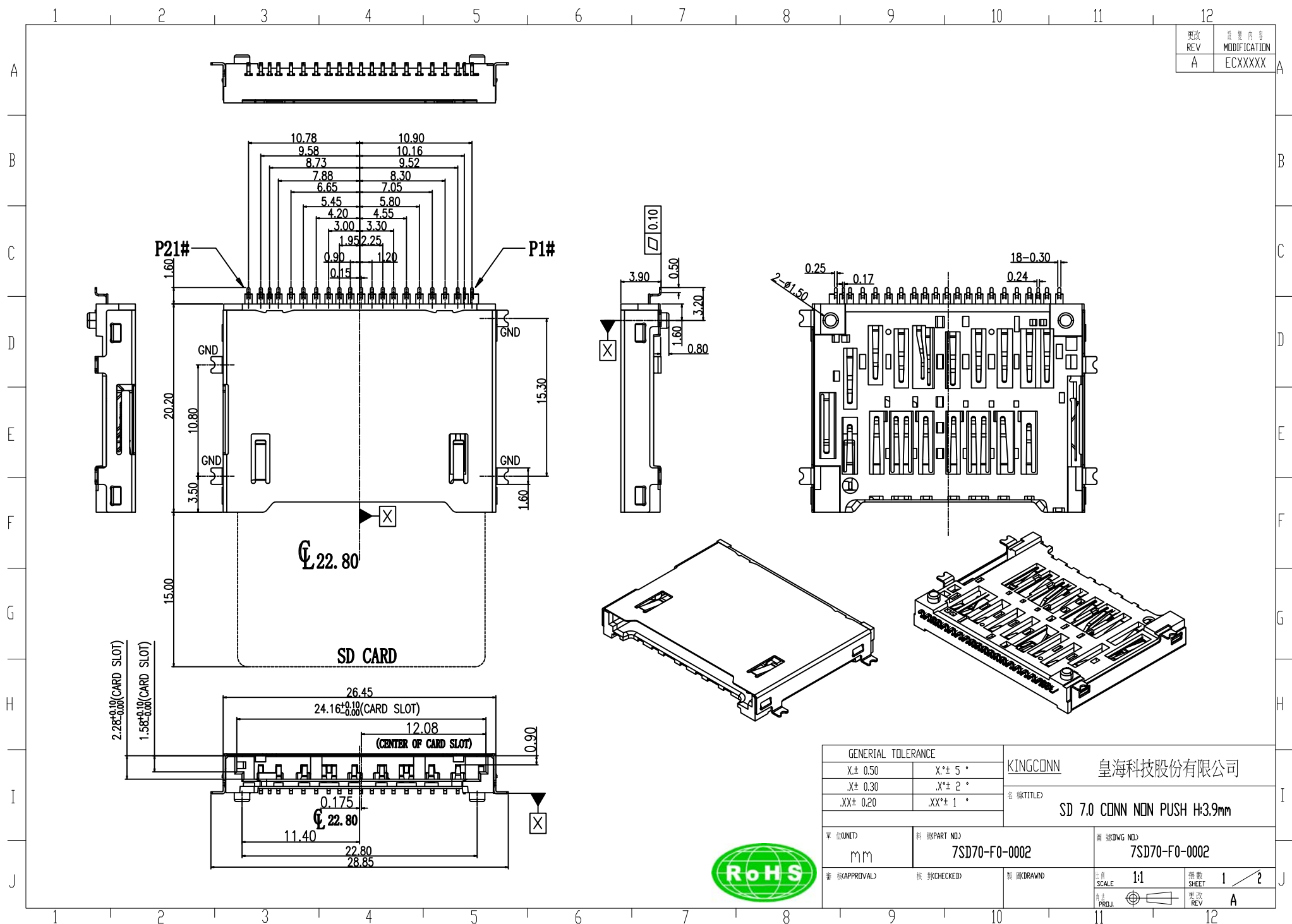


更改 REV	修改内容 MODIFICATION
A	ECXXXXX

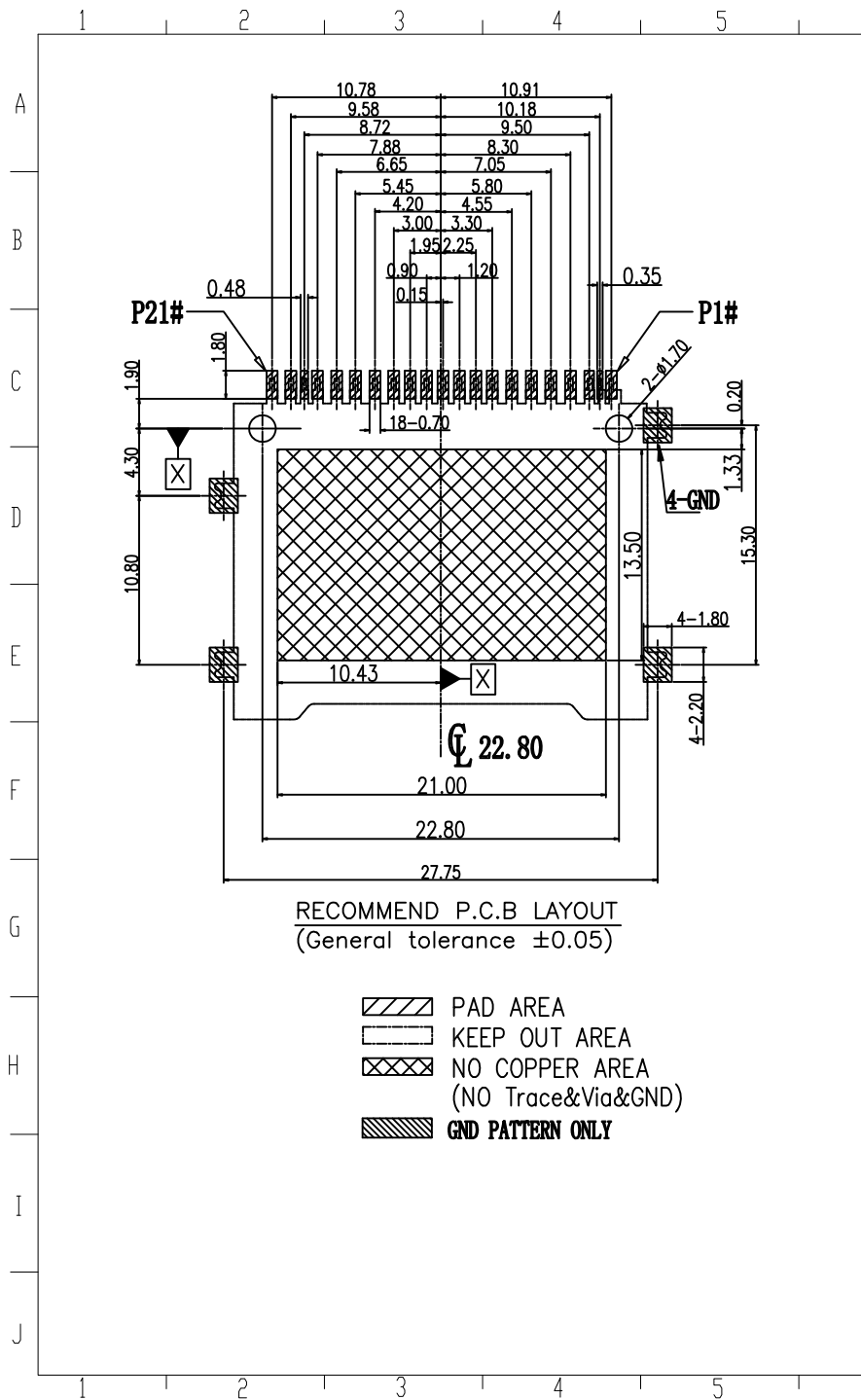


GENERAL TOLERANCE	
X± 0.50	X.*± 5°
X± 0.30	.X.*± 2°
.XX± 0.20	.XX.*± 1°

KINGCONN	皇海科技股份有限公司
名称/TITLE	SD 7.0 CONN NON PUSH H:3.9mm

单位/UNIT	料号/PART NO.	图号/DWG NO.
mm	7SD70-F0-0002	7SD70-F0-0002
审核/APPROVAL	检查/CHECKED	制图/DRAWN
比例/SCALE	1:1	张数/SHEET
图号/PROJ.		1 / ?
		图式/REV
		A





RECOMMEND P.C.B LAYOUT
(General tolerance ± 0.05)

- PAD AREA
- KEEP OUT AREA
- NO COPPER AREA
(NO Trace&Via&GND)
- GND PATTERN ONLY

NOTES:

- 1.Operation Voltage :10V AC
- 2.Current rating:1.0A Max
- 3.Contact resistance:100m Ω MAX
- 4.Dielectric withstanding voltage:500V AC/1minute
- 5.Insulation resistance:1000M Ω min 500V DC
- 6.Operation temperature:-20°C to 60°C
- 7.Storage temperature:-20°C to 70°C
- 8.Durability:10,000 CYCLES
- 9.PLATING:
 - 9-1 CONTACT: 1u" min.GOLD PLATED ON CONTACT AREA
80u" min.TIN(MATTE) PLATING ON SOLDER TAIL AREA
50u" min.NICKEL UNDERPLATING OVERALL
 - 9-2 SHELL :1u" min.GOLD PLATED ON SOLDER,30u" min. NICKEL PLATING OVERALL
- 10.Recommending A Metal More Than 0.15mm Thick.
Please Confirm Solderability, If Use A Metal Mask Less Than 0.15mm Thick.

No.	Pin Assignment					
	SD MODE		UHS II MODE		SD EXPRESS MODE	
P1			VSS3	SUPPLY VOLTAGE GROUND	VSS3	SUPPLY VOLTAGE GROUND
P2					RESERVED	RESERVED ARE FOR FUTURE PAD EXTENSION
P3	DATA2	DATA LINE BIT2			CLKREQ#	REFERENCE CLOCK REQUEST SIGNAL. ALSO USED BY L1 PM SUB-STATES
P4			DO+	DIFFERENTIAL DATA 0+	PCLE TX+	
P5	CD/DATA3	CARD DETECT/DATA LINE BIT2		NOT USED	PERST#	PE-RESET IS A FUNCTIONAL RESET
P6			DO-	DIFFERENTIAL DATA 0-	PCLE TX-	
P7	CMD	COMMAND/RESPONSE		NOT USED		NOT USED
P8	CD	CARD DETECT	CD	CARD DETECT	CD	CARD DETECT
P9	VSS1	SUPPLY VOLTAGE GROUND	VSS1	SUPPLY VOLTAGE GROUND	VSS1	SUPPLY VOLTAGE GROUND
P10			VSS4	SUPPLY VOLTAGE GROUND	VSS4	SUPPLY VOLTAGE GROUND
P11			VDD2	SUPPLY VOLTAGE (1.8V)	VDD2	SUPPLY VOLTAGE (1.8V)
P12	VDD	SUPPLY VOLTAGE	VDD1	SUPPLY VOLTAGE (3.3V)	VDD1	SUPPLY VOLTAGE (3.3V)
P13			D1-	DIFFERENTIAL DATA 1-	PCLE RX-	
P14	CLK	CLOCK		NOT USED		NOT USED
P15			D1+	DIFFERENTIAL DATA 1+	PCLE RX+	
P16	VSS2	SUPPLY VOLTAGE GROUND	VSS2	SUPPLY VOLTAGE GROUND	VSS2	SUPPLY VOLTAGE GROUND
P17			VSS5	SUPPLY VOLTAGE GROUND	VSS5	SUPPLY VOLTAGE GROUND
P18	DAT0	DATA LINE BIT0	RCLK+		REFCLK+	PCLE REF CLOCK
P19					VDD2	SUPPLY VOLTAGE (1.2V)-TBD
P20	DAT1	DATA LINE BIT1	RCLK-		REFCLK-	PCLE REF CLOCK
P21	WP	WRITE PROTECT	WP	WRITE PROTECT	WP	WRITE PROTECT

GENERAL TOLERANCE		KINGCONN 皇海科技股份有限公司
X \pm 0.50	X* \pm 5°	
.X \pm 0.30	.X* \pm 2°	
.XX \pm 0.20	.XX* \pm 1°	名 稱(TITLE) SD 7.0 CONN NON PUSH H:3.9mm
單 位(UNIT) mm	料 號(PART NO.) 7SD70-F0-0002	圖 號(DWG NO.) 7SD70-F0-0002
審 核(APPROVAL)	核 對(CHECKED)	製 圖(DRAWN)
SCALE 1:1		張 數(SHEET) 2 / 2
PROJ.		圖 號(REV) A

